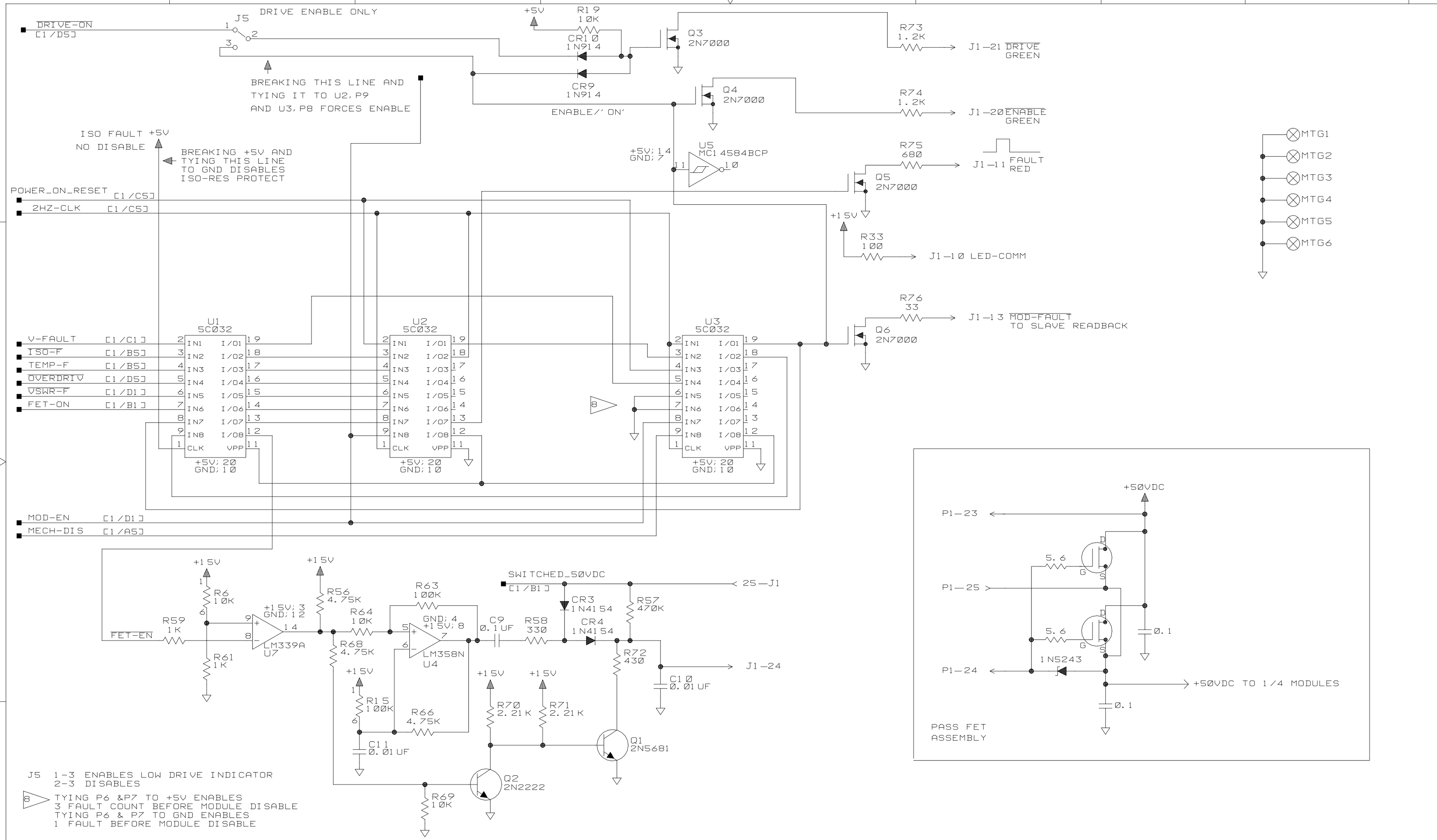

J5 1-3 ENABLES LOW DRIVE INDICATOR
2-3 DISABLES

8 TYING P6 &P7 TO +5V ENABLES
3 FAULT COUNT BEFORE MODULE DISABLE
TYING P6 & P7 TO GND ENABLES
1 FAULT BEFORE MODULE DISABLE

NOTES

4. INDUCTANCE IN UH.
3. CAPACITANCE IN UF.
2. RESISTANCE IN OHMS.
1. ALL RESISTORS ARE 1/2 WATT, 1%.

UNLESS OTHERWISE NOTED:

6. 5% RESISTORS MAY BE SUBSTITUTED FOR ANY RESISTOR UNLESS LABELED 1% ON THIS DOCUMENT.
5. SHEET NUMBER / ZONE — [2/A1] — CONTINUATION SYMBOL

REVISION LTR	ZONE	DATE	DFTM	ENG	ECO NUM
A		1-10-94	DFD	JLS	VMI-09
REVISE CIRCUIT					
F		7-17-98	DFD	DFD	42263
CHG U8.U9 TO Q3,Q4,Q5,Q6					

HARRIS

HARRIS CORPORATION
BROADCAST DIVISION
P.O. BOX 4290
QUINCY, ILLINOIS 62305

THIS DOCUMENT CONTAINS PROPRIETARY DATA OF HARRIS CORPORATION. NO DISCLOSURE, REPRODUCTION, OR USE OF ANY PART THEREOF MAY BE MADE EXCEPT BY WRITTEN PERMISSION.

DRAWN BY	J SECCIA
DATE	11-24-93
ENG CHK	D NICKELL
PROJ ENG	D DANIELSONS
MFG ENG	R CROCKETT
D	SHEET 2 OF 2

TITLE: SCHEM, MODULE CONTROL BD

DWG # 839-7900-700 REV F